



The microcontroller's 20 GPIO pins are divided among the memory mapped I/O (13 pins) and important peripheral functions (7 pins).

The memory mapped I/O uses 5 unidirectional address bits (32 addressable peripherals), and 8 bidirectional data bits.

Important events (UART recv, Timer interrupt, etc.) are not routed through the memory mapped I/O.

All busses 8 bits unless otherwise indicated.